

DESIGN AND SIMULATION OF FULL-BRIDGE CONVERTER FOR BATTERY CHARGING SYSTEM

P.ANJALI KUMARI¹, S.BAJI² N.HANUMA NAIK³

¹M.Tech Student, Dept of EEE, Sri Chundi Ranganayakulu Engineering
College, Ganapavaram, Guntur, A.P, India.

²Associate Professor, Dept of EEE, Sri Chundi Ranganayakulu Engineering
College, Ganapavaram, Guntur, A.P, India.

³Associate Professor, HOD, Dept of EEE, Sri Chundi Ranganayakulu Engineering
College, Ganapavaram, Guntur, A.P, India

ABSTRACT:

This study aims to progress the development of a battery charger that is not connected to a power source. These gadgets are essential in energy stockpiling frameworks, since they execute battery charging in order to maintain their lifetime and health, as they are the primary source of the costs associated with these frameworks. Bidirectional Full-Bridge converters are used to implement circuit postulations in different works. Helper circuits are required for these converters in order to reduce their reversing difficulties. Although circuits like voltage clampers and snubbers are not well addressed in these works, their use is problematic for bidirectional activity. This study shows an exchange mechanism linked in a Full-Bridge converter to reduce overvoltage in semiconductors and make it suitable for use in bidirectional converters, as in the charging of battery banks, to eliminate these circuits in detachable converters..

Keywords – Isolated converters, charge controllers, battery chargers, and a snubber-less converter are all examples of this kind of device.

INTRODUCTION

Controlling the power flow between a voltage source and the batteries is essential to various battery charging methods. Using static converters, this control may be accomplished.

DC transmission from rectifier circuits or age sources may be modified in theory by changing the voltage levels between the rectifier circuits and the batteries. UPSs and half-breed age frameworks are common places to find this occurrence (Uninterrupted Power Supply). Energy stockpiling structures have a wide selection of converter configurations to choose from. Disengaged converters are used in many works to protect the battery bank and increase the converter's gain. These converters are required because of the wide voltage differences between the DC connectors and battery banks. Additionally, battery banks ranging from 12 V to 60 V are often employed in UPS media transmission frameworks to create DC connections ranging from 200 V to 400 V.

To avoid the need of several structures for different battery ways, these converters allow you to comprehend the charge and release of batteries in one gear. Two converters are required to operate as either an inverter or a voltage rectifier in this particular situation, depending on the activity of the DC connection and the battery bank. Another distinguishing characteristic of Full-Extension converters is that they are often implemented as voltage-sustained converters, as seen in Fig. 1. Since the current in the converter increases rapidly when two switches are being directed at the same time due to trading mistakes or switches switching on at the same time, these converters have fundamental concerns identified with overcurrent. As a pre-requisite to making effective use of these converters,

Dead-band snubbers or more improve exchange operations by ensuring that switches in an arrangement do not enter into directing at the same time, which is known as snubbing. Assistant circuits, such as RCDs or dynamic clampers, are unnecessary since they may affect the activity of the converter as a voltage rectifier for most of the time.

In the case of a Full-Bridge converter, dynamic cinched approaches are not appealing since they increase the number of switches and make the system more complicated. A battery charger with a Full-Bridge converter is used to demonstrate the viability of using these inverters in bidirectional converters in this study. In order to avoid the need of snubbers or clampers, the primary goal is to reduce the number of circuit components and keep up the correct operation of circuits, which reduces overvoltage at the semiconductors and transformers.

PROPOSED CIRCUIT

A few factors, such as the charging of batteries, must be taken into account in the improvement of the converter because of the various situations. Outline components that need to be highlighted include: more pronounced voltage contrasts between DC connection and battery bank, temperatures of activity and current and voltage swells. DC connection The battery's swell of current and voltage are the most fundamental of these components.

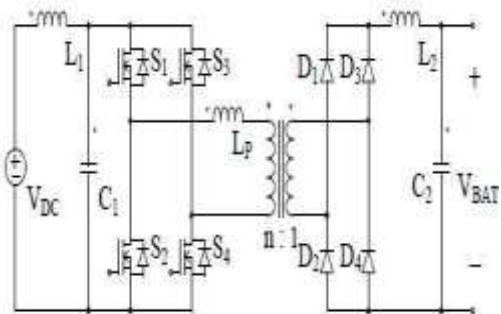


Fig.2. Proposed Circuit With Full-Bridge Converter

The following thoughts are included in the analysis of the circuit:

Assuming C_1 's voltage is constant and equal to V_{DC} , the switches and diodes are supposed to be almost flawless, and the charging inductance of transformer is assumed to be large enough to be disregarded; d) the dead band between switches is assumed to be negligible. In four phases, the non-secluded Buck converter serves as a voltage reduction. Static increases, such as those seen with a Buck converter, appear to be entirely normal in this context.

The first step in the process is called "stage one." T0-T1: Fig. 3a With its primary goal being to apply a positive voltage to the transformer, an inverter Full-Bridge is first used in this process. A switch in conduction is used for this whereas a switch blocker is used for the other two. Secondary transformer diodes D2 and D3 charge the inductor L2 and capacitor C2 by conducting current. They expected V_{BAT} and V_{DC} voltages for the blocked switches, which is why diodes D1 and D4 are blocked. As shown in Fig. 3-a, the equivalent circuit of this step has been analysed.

so that it may reach a usable level of heating

$$IV = V_{DC} - V \dots \dots \dots (1)$$

batteries. The beat has been replaced with DC interface.

L_2 k

BAT

In addition, current is a key element since it might cause electromagnetic interference with the source. Thus, the battery has an incentive to introduce a lower charge current, as well as lower DC connect current, as a result of this arrangement. A yield channel L_2 and C_2 and an information LC channel L_1 and C_1 are suggested for this purpose.

$$iL_2 = i_{in}. k = i_{c2} \dots \dots \dots (2)$$

Fig. 3-b, T1-T2: T1-T2 In this stage, the transformer must be connected to a source of zero volts. In order to prevent DC link interference from affecting the remainder of the circuit, the switch S1 is disabled. The switch S2 is conducting with the goal of creating a conduit for surplus current at the transformer. Overvoltage in blocked

semiconductors may be avoided in this manner. Switches S1 and S3 have a voltage of VDC, but switches S2 and S4 have no voltage, since they are barred from operation. Analysis of this phase has led to the following conclusion:

$$V_{L2} = -V_{BAT} \quad (3)$$

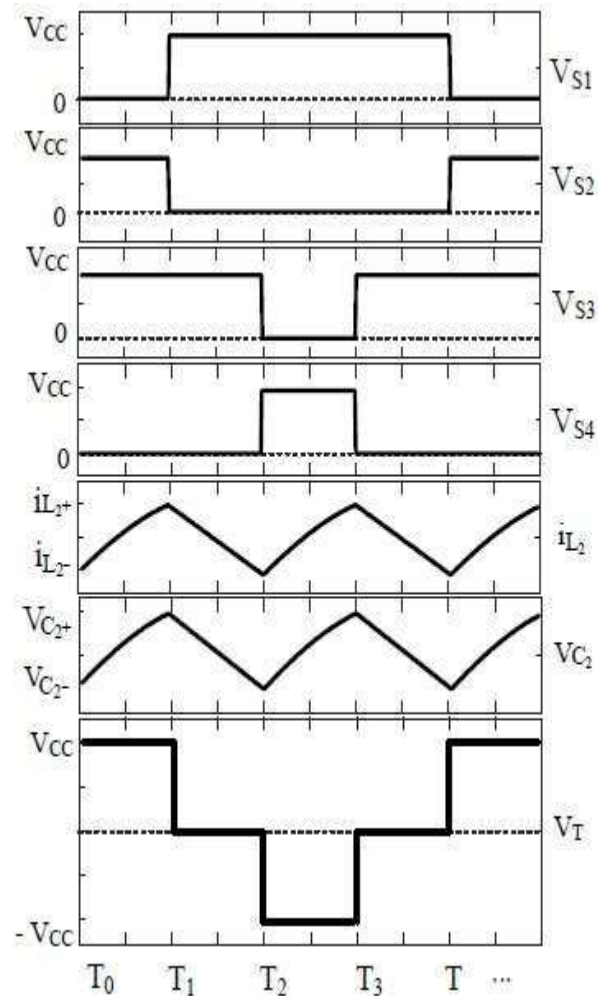
$$i_{L2} = i_{c2} - i_{BAT} \dots \dots \dots (4)$$

Switches S1 and S4 are blocked, while S2 and S3 are in conduction, resulting in a voltage equal to VDC being delivered to the transformer. With a reverse voltage of VBAT, the diodes D1 and D4 are conducting, while D2 and D3 are blocked. Similar to the previous phase, L2 and C2 are re-charged in this step as well. In this case, both equations (1) and (2) hold true.

T3-T is shown in Fig. 2-d, at the very end of the process, with a voltage of zero. During this time, just the switch S2 and not S3 are allowing current to flow. Similar to stage 2, the S4 switches are conducting, allowing the transformer's current to flow in a different direction. Using the formulas in equations (3) and (4), we can see that we have discharged the output filters L2 and C2 (4).

The inclusion of these stages is essential to the proper functioning of the converter, which requires an average voltage equal to zero to be applied to the transformer. In this manner, it is feasible to prevent the core of the transformer from being saturated. Therefore, the stages 1 and 3 times must be identical, much as the stages 2 and 4 times. As a reminder, it's important to notice that the output filter is collecting energy throughout periods T0-T1 and T2-T3. This is why the current era is referred to as the TON. Because T0-T1 and T2-T3 are also identical, the duty cycle of the converter is defined as $D = 2 \text{ TON} / T$.

Analyzing the converter's equations at each step of operation is important to determine the converter's static gain. When the voltage across the inductor L2 is equal to zero for the whole duration of operation of the converter, as shown by Eq. (5), it follows from Eqs. (1) and (3) that the static gain of converter.



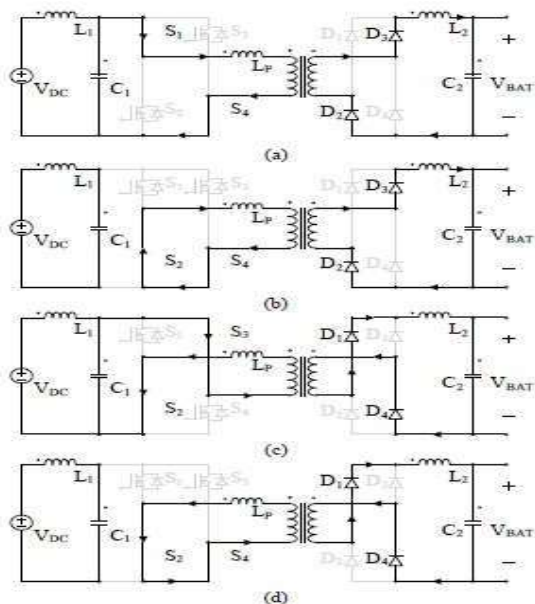


Fig.2. Equivalent circuits of the stages of operation of the converter in Buck mode.

$$0 = \left(\frac{V_{DC}}{k} - V_{BAT} \right) \cdot D + \left(\frac{V_{BAT}}{D} \right) \cdot (1 - D) \dots (5)$$

$$\frac{V_{BAT}}{D} = \dots (6)$$

Fig.3. Output Waveforms of Converter.

CONCLUSION

An isolated battery charger based on a Full-Bridge inverter was studied and designed in this paper. To get a high gain in the converter and to protect the battery bank, this property is essential in this sort of application.

Converter function is to reduce input voltage from DC link of 200V into battery bank's appropriate voltage.

It was found that the suggested converter was able to charge the batteries appropriately, delivering low ripple voltage and current, which is suitable for extending the battery life. A pulsed current may be avoided at the input by using the input filter, which also reduces interference on the DC connection.

It is feasible to confirm that the actual results of the converter match the theoretical waveforms by looking at the voltage waveforms on the switches and the transformer.

In addition, the suggested modulation avoids semiconductors from being overvoltaged without the need of snubbers or voltage clampers. Bidirectional converters are one of the main goals of this research, and the conversion is able to meet these requirements.

REFERENCES

Access in 2017 to Magna-Power "Current-Fed Power Processing Technology" overview, which includes current-fed power processing.

There are two books about batteries: [2] "Handbook of Batteries" 4^{ed} by D. Linden and T. B Reddy, McGraw-Hill Professional, New York, 2011.

IJSETR, Volume 4, No. 5, 2015. [3] C. Jabayabalu and K. Sarbham, "Single Stage High Gain Boost Converter With Battery Commutation in Solar Power Applications," 2015.

"A High Gain Input-Parallel Output-Series DC/DC Converter with Dual Coupled-Inductors" appeared in IEEE Transactions on Power Electronics Vol. 30, No. 3, 2015.